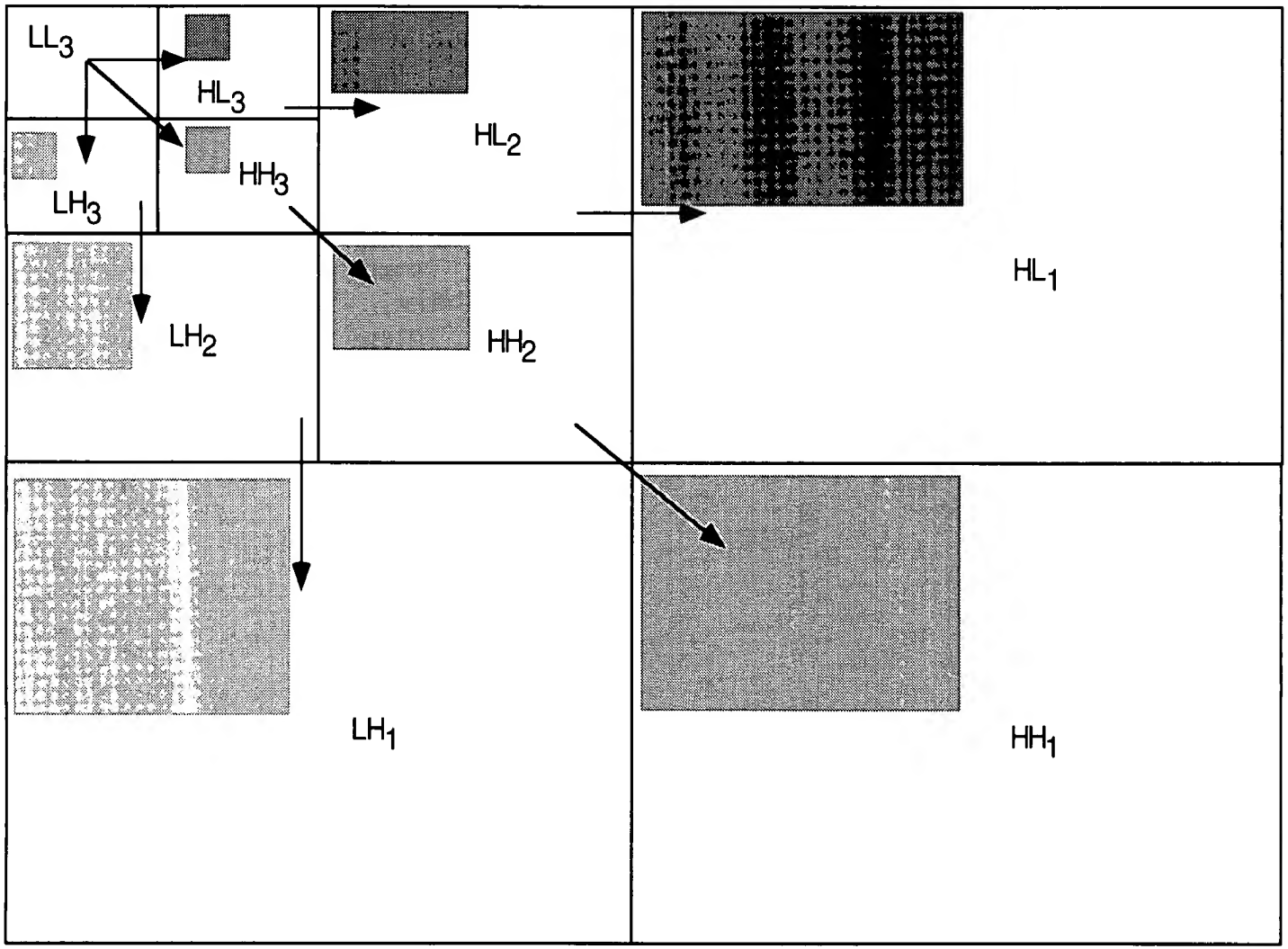


FIG. 1 (PRIOR ART)

200

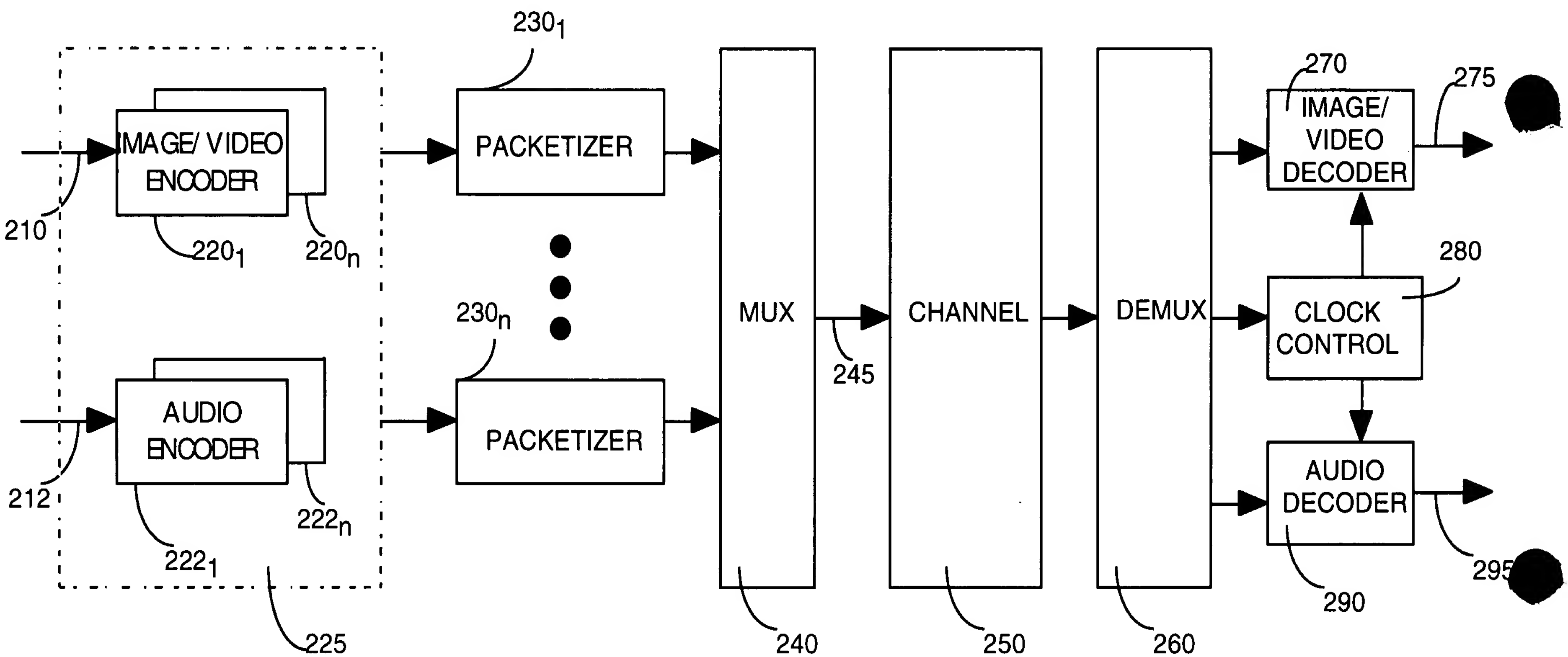

210
212
IMAGE/ VIDEO ENCODER
220_1
220_n
AUDIO ENCODER
222_1
222_n
225
230_1
PACKETIZER
230_n
PACKETIZER
MUX
240
245
CHANNEL
250
DEMUX
260
270
IMAGE/ VIDEO DECODER
275
CLOCK CONTROL
280
AUDIO DECODER
290
295

FIG. 2

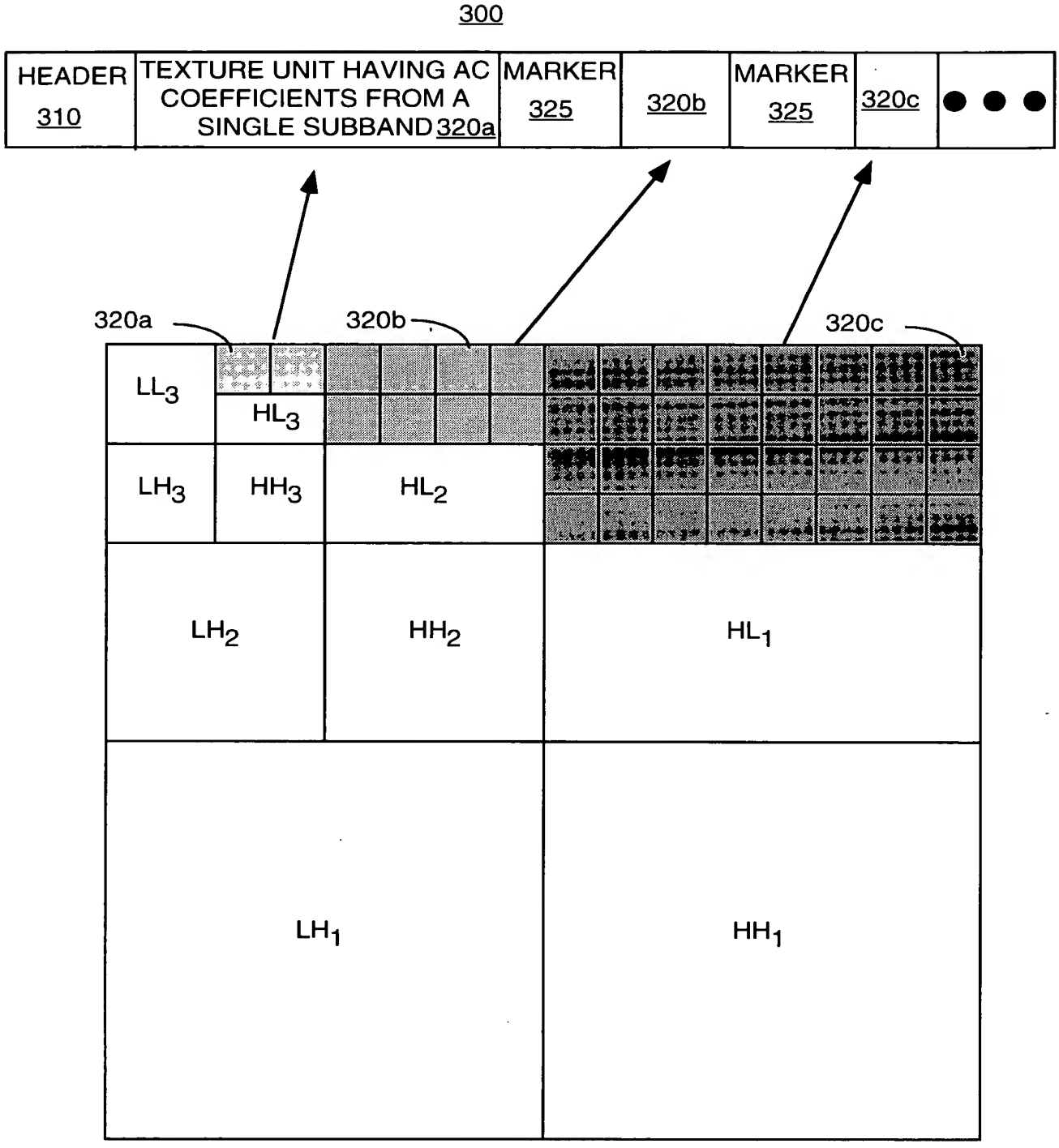

300
HEADER 310
TEXTURE UNIT HAVING AC COEFFICIENTS FROM A SINGLE SUBBAND 320a
MARKER 325
320b
MARKER 325
320c
320a
320b
320c
LL3
HL3
LH3
HH3
HL2
LH2
HH2
HL1
LH1
HH1

FIG. 3

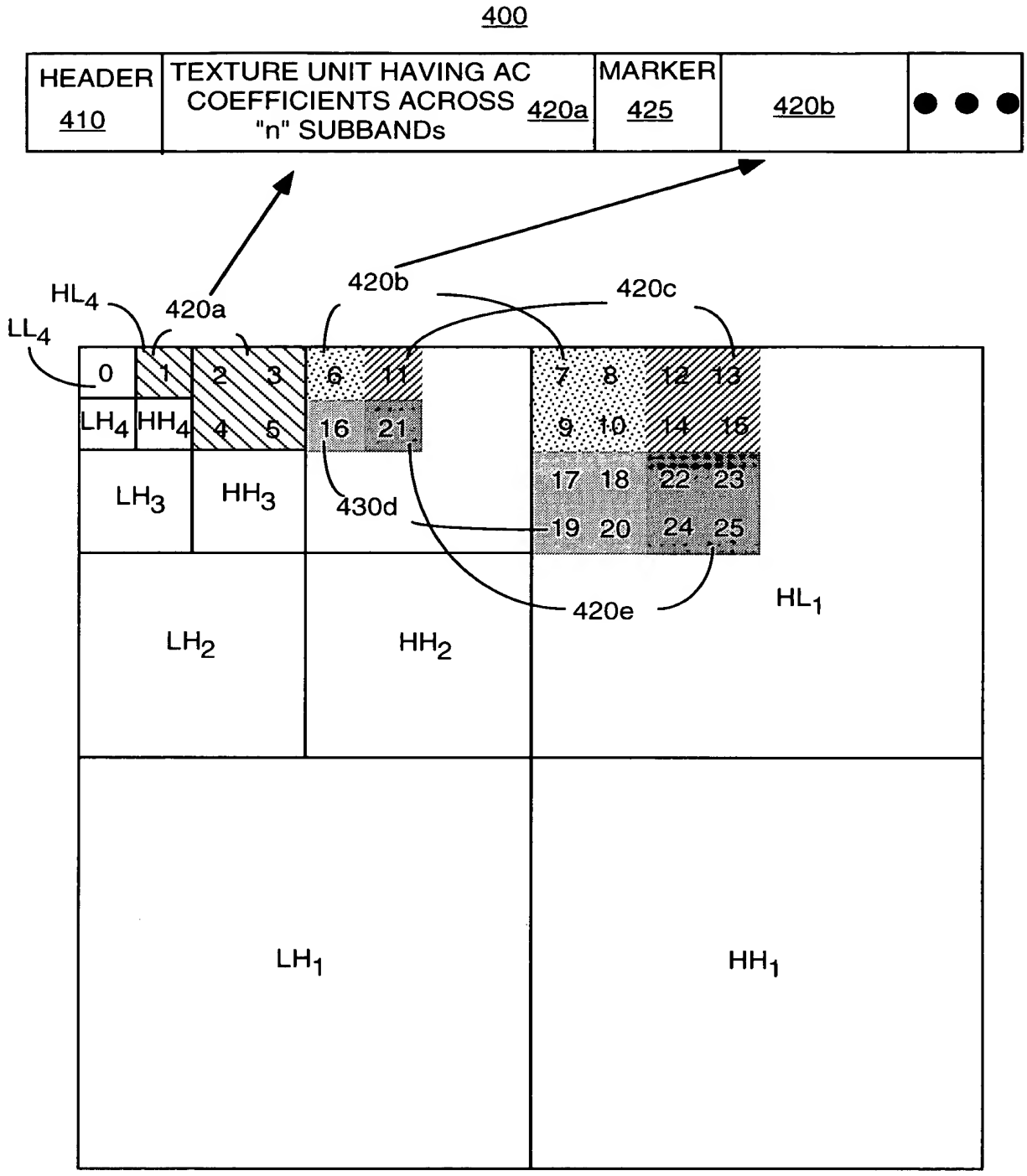
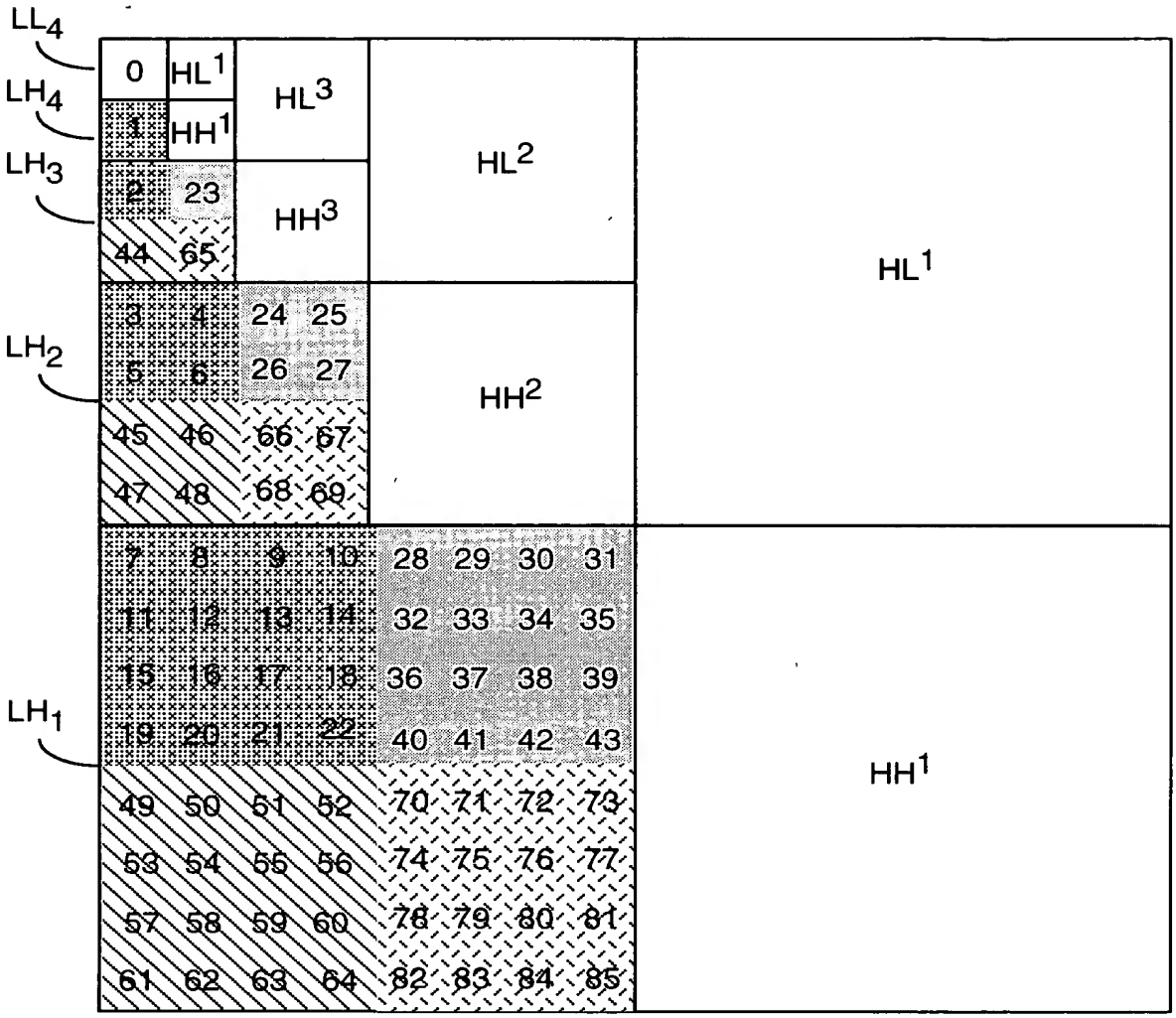

400
HEADER
410
TEXTURE UNIT HAVING AC COEFFICIENTS ACROSS "n" SUBBANDs
420a
MARKER
425
420b
HL4
LL4
420a
420b
420c
0
1
2
3
6
11
7
8
12
13
LH4
HH4
4
5
16
21
9
10
14
15
17
18
22
23
LH3
HH3
430d
19
20
24
25
420e
HL1
LH2
HH2
LH1
HH1

FIG. 4


LL4
LH4
LH3
LH2
LH1
0
HL1
1
HH1
HL3
2
23
HH3
44
65
HL2
3
4
24
25
5
6
26
27
HH2
45
46
66
67
47
48
68
69
HL1
7
8
9
10
28
29
30
31
11
12
13
14
32
33
34
35
15
16
17
18
36
37
38
39
19
20
21
22
40
41
42
43
HH1
49
50
51
52
70
71
72
73
53
54
55
56
74
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84
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FIG. 5

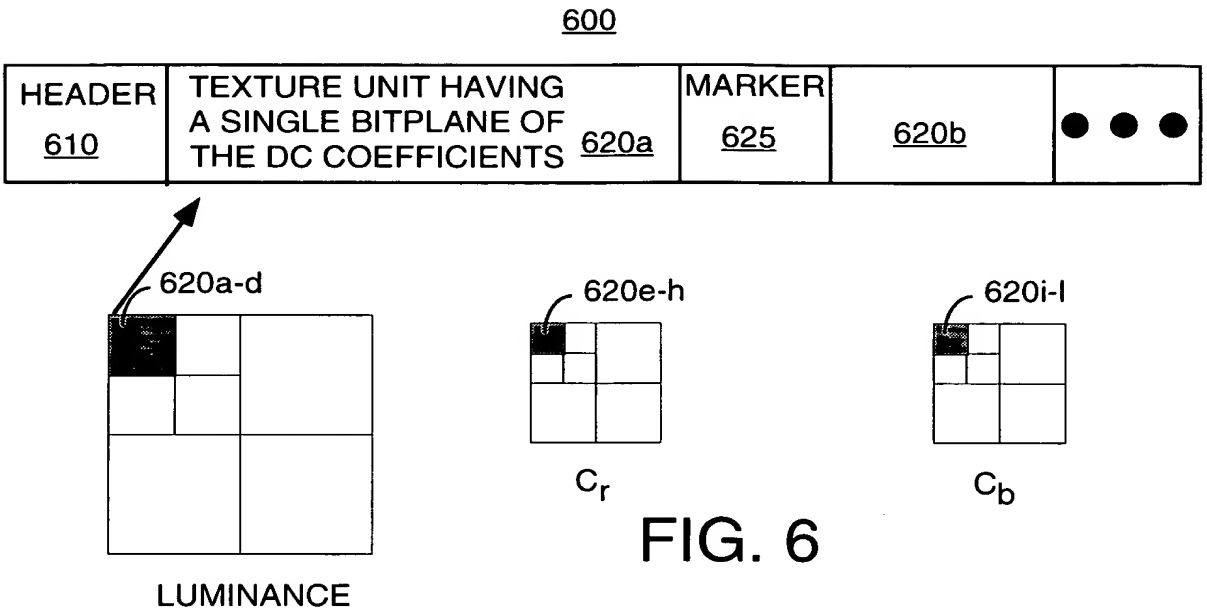
600
HEADER
610
TEXTURE UNIT HAVING
A SINGLE BITPLANE OF
THE DC COEFFICIENTS
620a
MARKER
625
620b
620a-d
620e-h
620i-l
C_r
C_b
LUMINANCE

FIG. 6

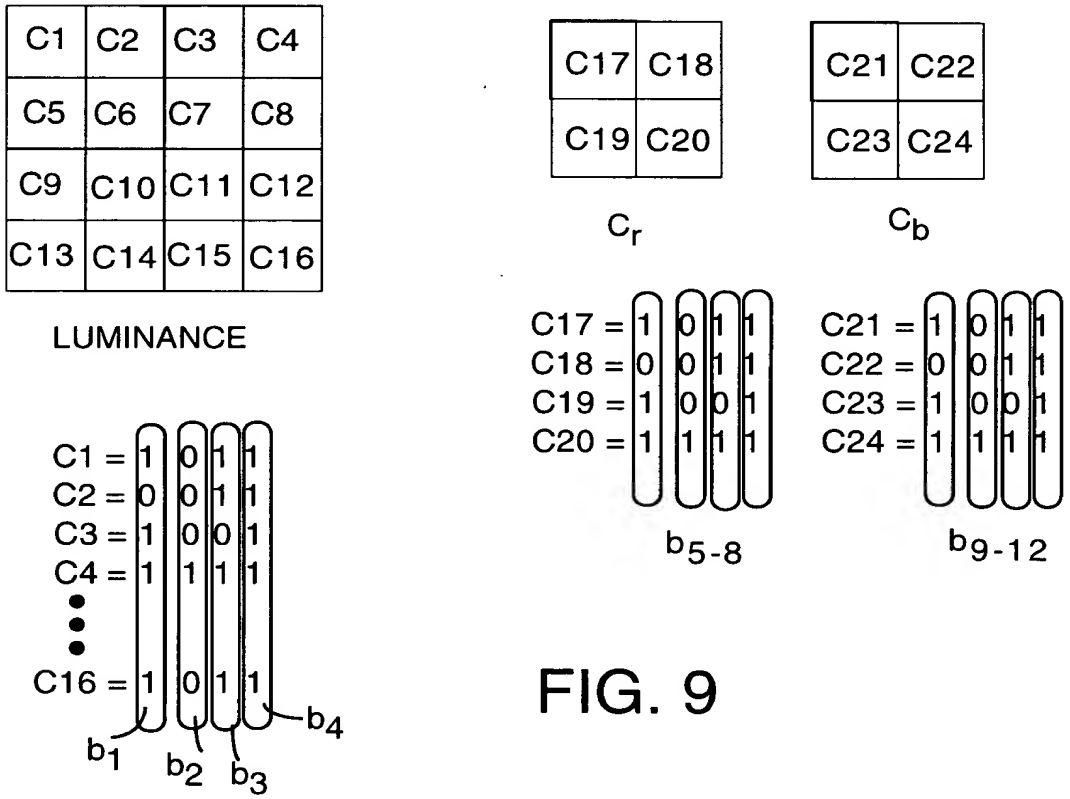
C1 C2 C3 C4
C5 C6 C7 C8
C9 C10 C11 C12
C13 C14 C15 C16
LUMINANCE
C17 C18
C19 C20
C_r
C21 C22
C23 C24
C_b
C1 = 1 0 1 1
C2 = 0 0 1 1
C3 = 1 0 0 1
C4 = 1 1 1 1
C16 = 1 0 1 1
b_1 b_2 b_3 b_4
C17 = 1 0 1 1
C18 = 0 0 1 1
C19 = 1 0 0 1
C20 = 1 1 1 1
b_{5-8}
C21 = 1 0 1 1
C22 = 0 0 1 1
C23 = 1 0 0 1
C24 = 1 1 1 1
b_{9-12}

FIG. 9

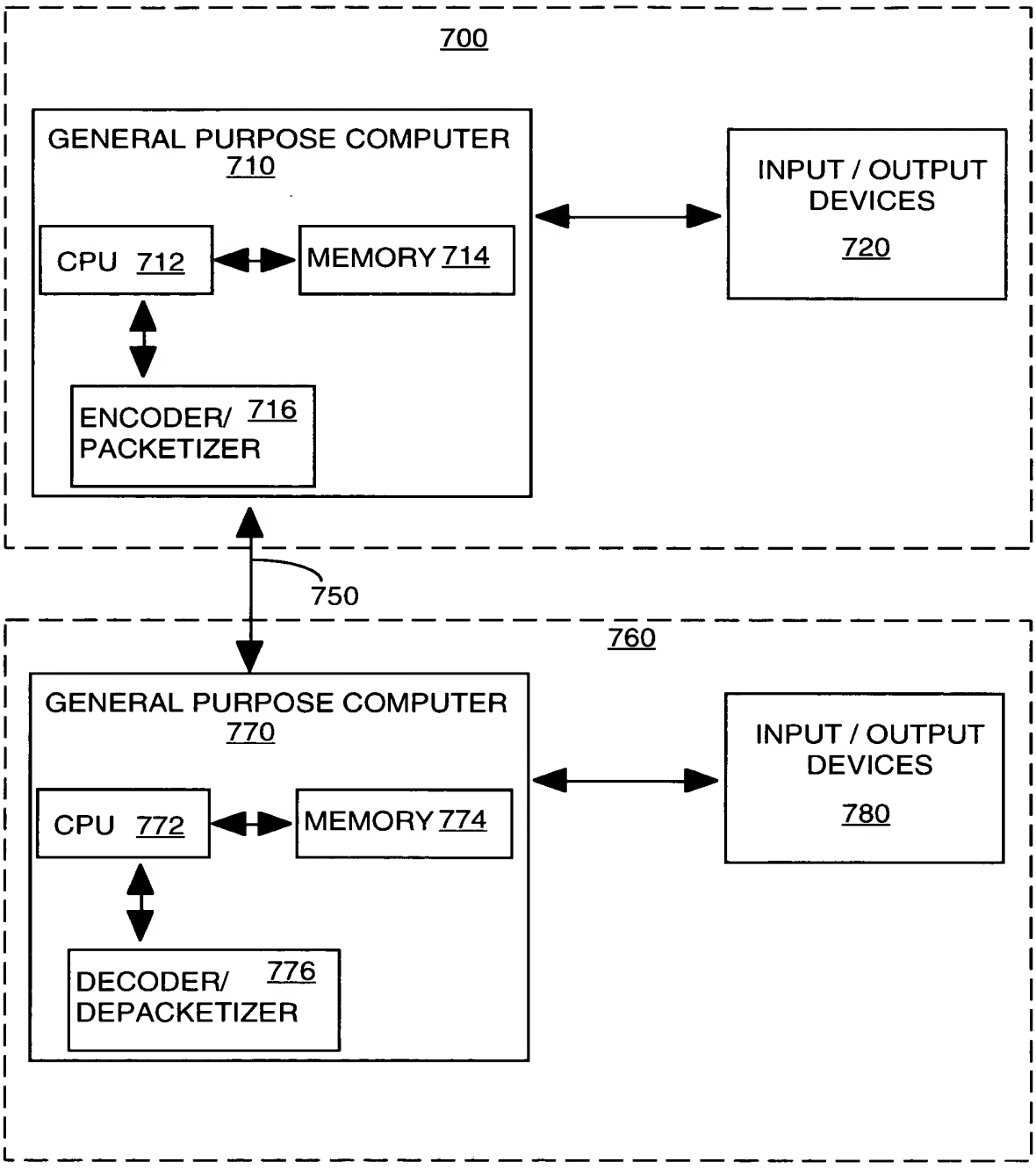
700
GENERAL PURPOSE COMPUTER
710
CPU 712
MEMORY 714
ENCODER/ 716
PACKETIZER
INPUT / OUTPUT
DEVICES
720
750
760
GENERAL PURPOSE COMPUTER
770
CPU 772
MEMORY 774
DECODER/ 776
DEPACKETIZER
INPUT / OUTPUT
DEVICES
780

FIG. 7

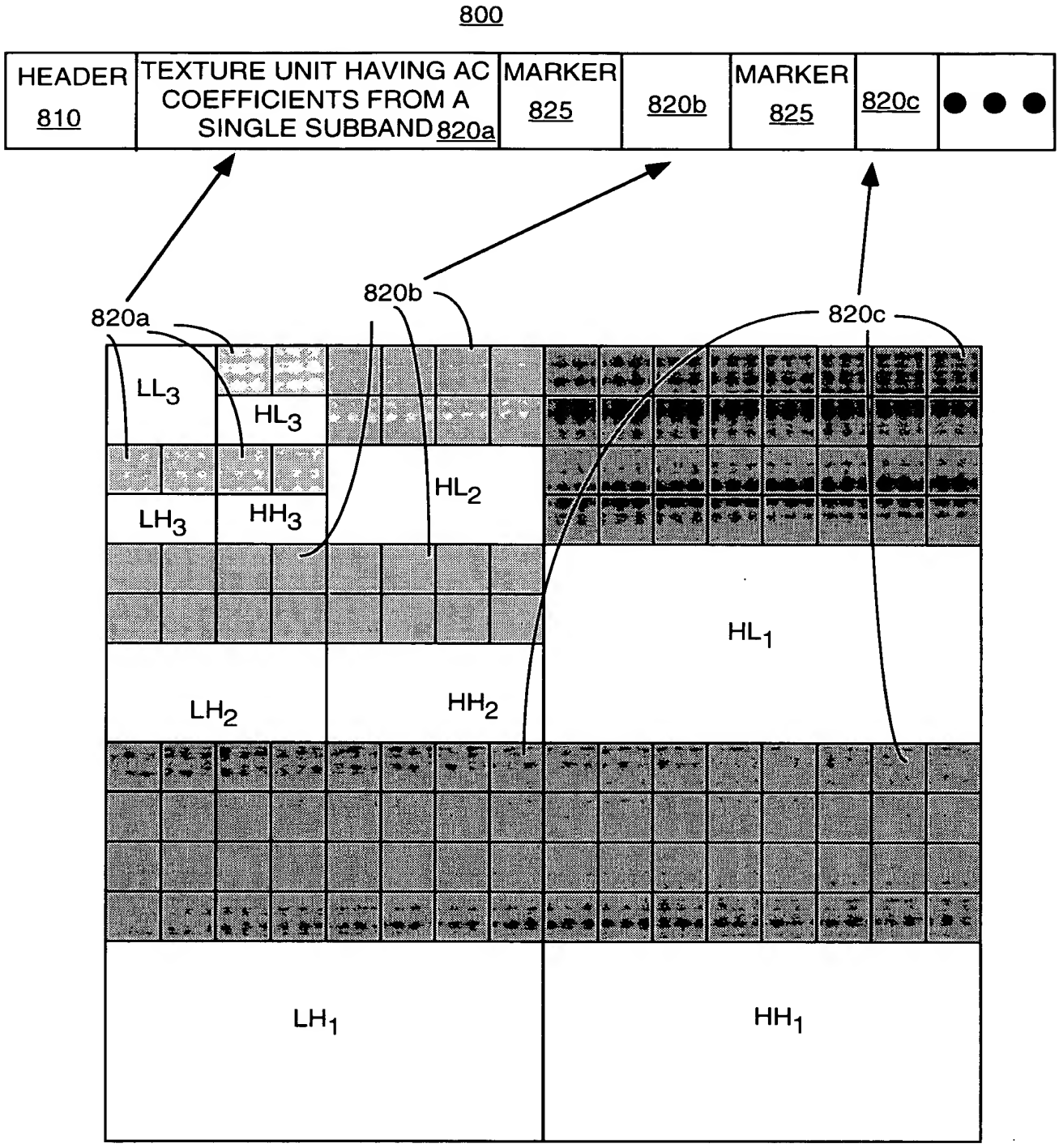
800
HEADER
810
TEXTURE UNIT HAVING AC COEFFICIENTS FROM A SINGLE SUBBAND
820a
MARKER
825
820b
MARKER
825
820c
820a
820b
820c
LL3
HL3
LH3
HH3
HL2
HL1
LH2
HH2
LH1
HH1

FIG. 8